

<b>Notice of References Cited</b>	Application/Control No. 10/757,882	Applicant(s)/Patent Under Reexamination CARUK, GORDON F.	
	Examiner Gopal C. Ray	Art Unit 2111	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,113,369	05-1992	Kinoshita, Tsuneo	710/307
*	B	US-4,554,657	11-1985	Wilson, Dayton H.	370/364
*	C	US-4,703,198	10-1987	Porter et al.	326/86
*	D	US-6,731,134	05-2004	Bucossi et al.	326/56
*	E	US-5,767,701	06-1998	Choy et al.	326/93
*	F	US-2004/0172494	09-2004	Petty et al.	710/305
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"Introduction to PCI Express-a new high speed serial bus" by Dhawan, S.K. (abstract only) □ □ Publication Date: 23-29 Oct. 2005
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.